Narasimhan Venkatesh
Ph:+91 9849456036
Email: n.venkatesh.nv@gmail.com

Narasimhan Venkatesh is a seasoned technology leader with nearly four decades of engineering and management experience in the semiconductor, wireless connectivity, IoT, and electronic systems industries. He is recognized for building and leading high-performing R&D, product development, and engineering teams, particularly for US-headquartered companies operating in India. Venkatesh holds 22 US patents and is a Fellow of the Indian National Academy of Engineering, reflecting his deep technical expertise and contributions to advancing technology.

In his current role at Silicon Labs (Hyderabad), he leads APAC R&D initiatives, collaborating with cross-functional teams across VLSI design, analog/RF, wireless protocols, SoC platforms, system validation, and automation. He also serves as a subject-matter expert on Wi-Fi technologies, contributing to IEEE 802.11 standardization and Wi-Fi Alliance interoperability efforts. Alongside his industry responsibilities, Venkatesh actively fosters academia-industry partnerships and has helped establish IoT Centres of Innovation at over ten academic institutions.

Previously, at Redpine Signals, he set up and led the India Development Centre, driving chip design, protocol implementation, product management, and applications engineering teams. At Paxonet Communications, he introduced IP-based design flows and spearheaded the development and market introduction of multiple telecom and optical networking chips. His early career began with Hindustan Aeronautics Ltd., where he designed jam-resistant communication systems and custom ASICs for secure military communications.

Beyond his corporate roles, Venkatesh is a long-standing volunteer with IEEE, holding leadership positions across technical societies and contributing extensively to professional events. He is also a prolific writer and speaker, having authored numerous technical articles and delivered talks at international conferences and workshops.