

Workshop Agenda

31st October 2025 - Day 1 (Friday)

09:00 - 09:30 Inauguration Ceremony
09:30 - 10:30 Introduction to Design for Test-1
10:30 - 11:00 Tea/Coffee Break
11:00 - 12:30 Introduction to Design for Test-2
12:30 - 13:30 Networking & Lunch
13:30 - 14:45 Fault Models
14:45 - 15:00 Tea/Coffee Break
15:00 - 16:00 Fault Simulation and
Test Generation
16:00 - 17:00 DFT as a career

1st November 2025 - Day 2 (Saturday)

09:00 - 10:30 Scan based Testing
10:30 - 11:00 Tea/Coffee Break
11:00 - 12:30 Scan Compression & Logic BIST
12:30 - 13:30 Networking & Lunch
13:30 - 14:30 Test Diagnosis
14:30 - 15:30 Memory Testing
15:30 - 15:45 Tea/Coffee Break
15:45 - 17:00 Test Standards
17:00 - 17:30 Short Project Discussion

2nd November 2025 - Day 3 (Sunday)

09:00 - 10:30 Lab Session 1
10:30 - 10:45 Tea/Coffee Break
10:45 - 12:30 Lab Session 2
12:30 - 13:30 Networking & Lunch
13:30 - 14:00 Valedictory Ceremony

Registration Fee

UG/PG Student: Rs. 400/-

Research/PhD Scholar: Rs. 550/-

Faculty Member: Rs. 750/-

Industry Professional: Rs. 1500/-

Registration fee includes Registration Kit,
Refreshment & Lunch.

Participants are requested to make their own
arrangements for accommodation if possible.
Limited number of participants can be provided
accommodation based on availability in
students hostel on paid basis.

Applications will be accepted on a first come
first serve basis.

Last Date for Registration: 25th October 2025

Account Details

Account Name:

School of Electronics Engineering,
A/C.No : 4915101000849,
Canara Bank, KIIT campus branch,
IFSC: CNRB0004915

For any queries, please contact:

Dr. Arindam Basak- M: 9938557233



3 days TTTC INDIA WORKSHOP On VLSI Testing

Date :

31st October-2nd November 2025

Time:

9:00 AM - 5:00 PM (on Friday & Saturday)

9:00 AM - 2:00 PM (on Sunday)

Venue:

School of Electronics Engineering, Campus 12,

Organized by

School of Electronics Engineering

In association with



Test Technology Technical Community (TTTC) India

Registration Link:

<https://forms.gle/x2of3yrGFidH4nY89>

Or Scan the QR code to Register



**KALINGA INSTITUTE OF
INDUSTRIAL TECHNOLOGY**

Deemed to be University
(Declared U/S 3 of UGC Act, 1956)
Bhubaneswar, Odisha, India

About KIIT

Kalinga Institute of Industrial Technology (KIIT) is a premier Indian university renowned for quality education, social commitment, and rapid growth—from humble beginnings to global recognition. Founded by Dr. Achyuta Samanta in 1992-93 with just ₹5,000, it now spans a 36-sq-km green campus with 25 schools, 3,000 faculty, and 15,000 staff. Over 30,000 students pursue UG, PG, and PhD programmes, along with 10,000 others in diverse streams, including 2,000 international students from 65+ countries. Accredited by ABET, IET, and NAAC A++, KIIT ranks 17th in India (NIRF 2025), 501-600 globally (THE 2026), and 257th in Asia (QS 2025).

About School of Electronics Engineering

The School of Electronics Engineering at KIIT-DU imparts cutting-edge skills through innovative teaching and research, led by highly qualified faculty who emphasize strong fundamentals and hands-on learning. Recognizing Electronics as the driver of future technologies—from 5G, IoT, and AI to Quantum and Smart Grids—the School offers B.Tech programs in Electronics & Telecommunication, Electronics & Electrical Engineering, Electronics & Computer Science, and VLSI Design & Technology, along with M.Tech specializations in VLSI Design & Embedded Systems, Communication Systems, Next-Gen Wireless Technology, and Automotive Electronics & Software (in collaboration with KPIT Technologies, India).

Key Features

- Fault Modeling
- Fault Simulation & Test Generation
- Design for Testability (DFT)
- Scan based Testing
- Test Diagnosis
- Memory Testing

Benefits

- Learn from Industry Leaders
- Lab Sessions on Industry Standard VLSI Testing Tools
- Networking Opportunities & Career Guidance
- Participation Certificate

Chief Patron

Prof. (Dr.) Achyuta Samanta
Founder, KIIT & KISS, Bhubaneswar

Patron

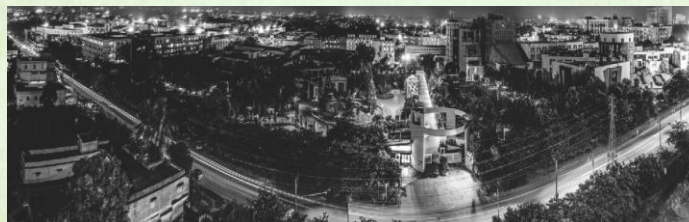
Prof. (Dr.) Saranjit Singh
Vice Chancellor, KIIT DU, Bhubaneswar
Prof. (Dr.) Raju K.D.
Pro Vice Chancellor, KIIT DU, Bhubaneswar
Prof. (Dr.) Jnyana Ranjan Mohanty
Registrar, KIIT DU, Bhubaneswar

Advisory Committee

Prof. (Dr.) Suprava Patnaik
Dean, School of Electronics Engineering,
KIIT DU, Bhubaneswar
Prof. (Dr.) Udai P Singh
Dean Research,
School of Electronics Engineering,
KIIT DU, Bhubaneswar
Prof. (Dr.) Abhishek Ray
Dean-Industry Engagement,
KIIT DU, Bhubaneswar

Coordinators

Dr. Arindam Basak
Associate Dean, Industry Engagement,
KIIT DU, Bhubaneswar **Dr. Manas Ranjan Tripathy**
Assistant Professor, School of Electronics
Engineering, KIIT DU, Bhubaneswar **Mr. Manish Verma**
Principal Consultant, KIIT DU, Bhubaneswar



About TTTC India

“IEEE Test Technology Technical Community (TTTC) is a very reputed council run by IEEE Computer Society working in the domain of VLSI Design and Test. The industry veterans of the VLSI testing field who are the major volunteers of TTTC are contributing for this workshop.”

About Workshop

“Over the past few years, TTTC India has been actively conducting 3-day workshops at universities across the country to promote awareness and understanding of Design-for-Testability (DFT) and Test. These workshops are designed to foster a strong research ecosystem and help build a skilled workforce in the domain.”

Industry Speakers



Gaurav Bhargava
Senior Director,
Technology, Qualcomm



Sameer Chillarige
Software Engineering
Director, Cadence



Venkata Rangam Totakura
Senior Director Design
Engineering, Infineon Technologies



Rajit Karmakar
Member of Technical
Staff, AMD

In association with:

